



Vishay Semiconductors

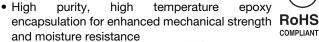
Schottky Rectifier, 18 A

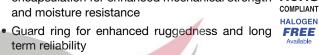


PRODUCT SUMMARY					
Package	TO-220AC				
I _{F(AV)}	18 A				
V _R	35 V, 40 V, 45 V				
V _F at I _F	0.53 V				
I _{RM} max.	25 mA at 125 °C				
T _J max.	175 °C				
Diode variation	Single die				
E _{AS}	24 mJ				

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation





- Designed and qualified according to JEDEC-JESD47
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-18TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	18	Α						
V_{RRM}	Range	35 to 45	V						
I _{FSM}	t _p = 5 μs sine	1800	А						
V _F	18 A _{pk} , T _J = 125 °C	0.53	V						
T _J	Range	- 55 to 175	°C						

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-18TQ035PbF VS-18TQ035-N3	VS-18TQ040PbF VS-18TQ040-N3	VS-18TQ045PbF VS-18TQ045-N3	UNITS				
Maximum DC reverse voltage	V _R								
Maximum working peak reverse voltage	V _{RWM}	35	40	45	V				

ABSOLUTE MAXIMUM RATING	is	E-L-E-	IRUI		2
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS		
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 149 °C, rectangular waveform			
Maximum peak one cycle non-repetitive surge current	lea	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1800	Α
See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	390	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 3.6 \text{A}, L = 3.7$	24	mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero Frequency limited by T_J maxim	3.6	А	



VS-18TQ0..PbF Series, VS-18TQ0..-N3 Series

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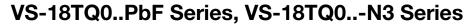
ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
Maximum forward voltage drop See fig. 1		18 A	T _{.1} = 25 °C	0.60				
	V _{FM} ⁽¹⁾	36 A	1j=25 C	0.72	V			
		18 A	T _{.1} = 125 °C	0.53				
		36 A	1	0.67				
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	2.5	mA			
See fig. 2		T _J = 125 °C	v _R = nateu v _R	25	IIIA			
Maximum junction capacitance	C_{T}	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		1400	pF			
Typical series inductance	Ls	Measured lead to lead 5 m	8	nH				
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

Note

 $^{^{(1)}}$ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}		- 55 to 175	°C				
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	1.50	°C/W				
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.50	C/VV				
Approximate weight			2	g				
Approximate weight			0.07	oz.				
Mounting torque minimum			6 (5)	kgf · cm				
maximum			12 (10)	(lbf ⟨ in)				
			18TC	Q035				
Marking device		Case style TO-220AC	18TC	2040				
			18TC	Q045				







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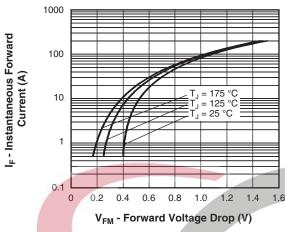


Fig. 1 - Maximum Forward Voltage Drop Characteristics

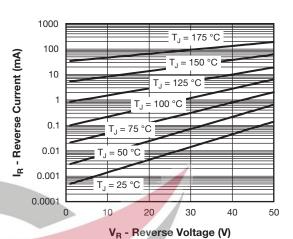


Fig. 2 - Typical Values of Reverse Current vs.

Reverse Voltage

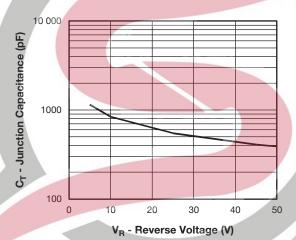


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

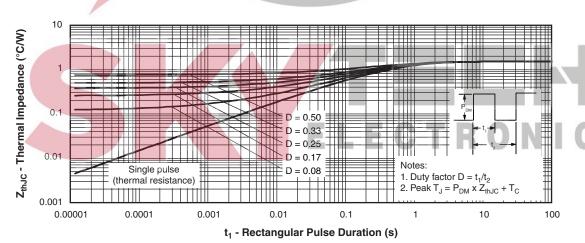


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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VS-18TQ0..PbF Series, VS-18TQ0..-N3 Series

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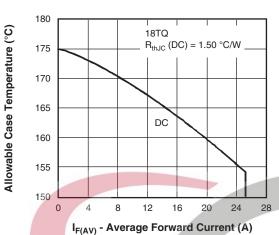


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

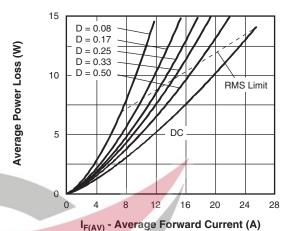


Fig. 6 - Forward Power Loss Characteristics

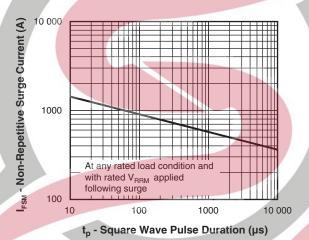


Fig. 7 - Maximum Non-Repetitive Surge Current

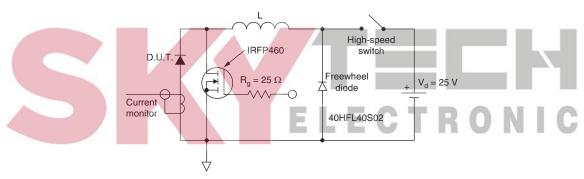


Fig. 8 - Unclamped Inductive Test Circuit

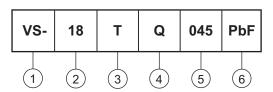




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ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

Current rating (18 = 18 A)

3 Package:

T = TO-220

Schottky "Q" series

035 = 35 V 040 = 40 V Voltage ratings

Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

045 = 45 V

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-18TQ035PbF	50	1000	Antistatic plastic tube						
VS-18TQ035-N3	50	1000	Antistatic plasti <mark>c tub</mark> e						
VS-18TQ040PbF	50	1000	Antistatic plastic tube						
VS-18TQ040-N3	50	1000	Antistatic plastic tube						
VS-18TQ045PbF	50	1000	Antistatic plastic tube						
VS-18TQ045-N3	50	1000	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS								
Dimensions		www.vishay.com/doc?95221						
Part marking information	TO-220AC PbF	www.vishay.com/doc?95224						
	TO-220AC -N3	www.vishay.com/doc?95068						
SPICE model		www.vishay.com/doc?95280						

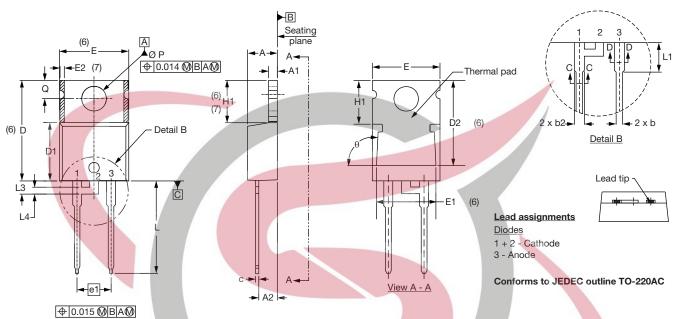
ELECTRO



Vishay Semiconductors

TO-220AC

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INC	HES	NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES		STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			E1	6.86	8.89	0.270	0.350	6
A1	1.14	1.40	0.045	0.055			E2	- /	0.76	-	0.030	7
A2	2.56	2.92	0.101	0.115			е	2.41	2.67	0.095	0.105	
b	0.69	1.01	0.027	0.040			e1	4.88	5.28	0.192	0.208	
b1	0.38	0.97	0.015	0.038	4		H1	6.09	6.48	0.240	0.255	6, 7
b2	1.20	1.73	0.047	0.068			L	13.52	14.02	0.532	0.552	
b3	1.14	1.73	0.045	0.068	4		L1	3.32	3.82	0.131	0.150	2
С	0.36	0.61	0.014	0.024			L3	1.78	2.13	0.070	0.084	
c1	0.36	0.56	0.014	0.022	4		L4	0.76	1.27	0.030	0.050	2
D	14.85	15.25	0.585	0.600	3		ØР	3.54	3.73	0.139	0.147	
D1	8.38	9.02	0.330	0.355	7		Q	2.60	3.00	0.102	0.118	
D2	11.68	12.88	0.460	0.507	6	EL	θ	90° t	o 93°	90° t	o 93°	
Е	10.11	10.51	0.398	0.414	3, 6							

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline





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Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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